

FIG. 1 is a block diagram of a computer system 100. The system 100 includes a central processing unit 102, a control memory (ROM) 101, a memory (RAM) 103, a speech synthesis unit 110, an external storage device 104, a D/A converter 105, an input unit 106, a display unit 107, a keyboard 108, a speaker 109, and a display 110. The central processing unit 102 is connected to the control memory 101, the memory 103, the speech synthesis unit 110, the external storage device 104, the D/A converter 105, the input unit 106, the display unit 107, the keyboard 108, and the speaker 109. The control memory 101 is connected to the central processing unit 102. The memory 103 is connected to the central processing unit 102. The speech synthesis unit 110 is connected to the central processing unit 102. The external storage device 104 is connected to the central processing unit 102. The D/A converter 105 is connected to the central processing unit 102. The input unit 106 is connected to the central processing unit 102. The display unit 107 is connected to the central processing unit 102. The keyboard 108 is connected to the central processing unit 102. The speaker 109 is connected to the central processing unit 102. The display 110 is connected to the central processing unit 102.

FIG. 1

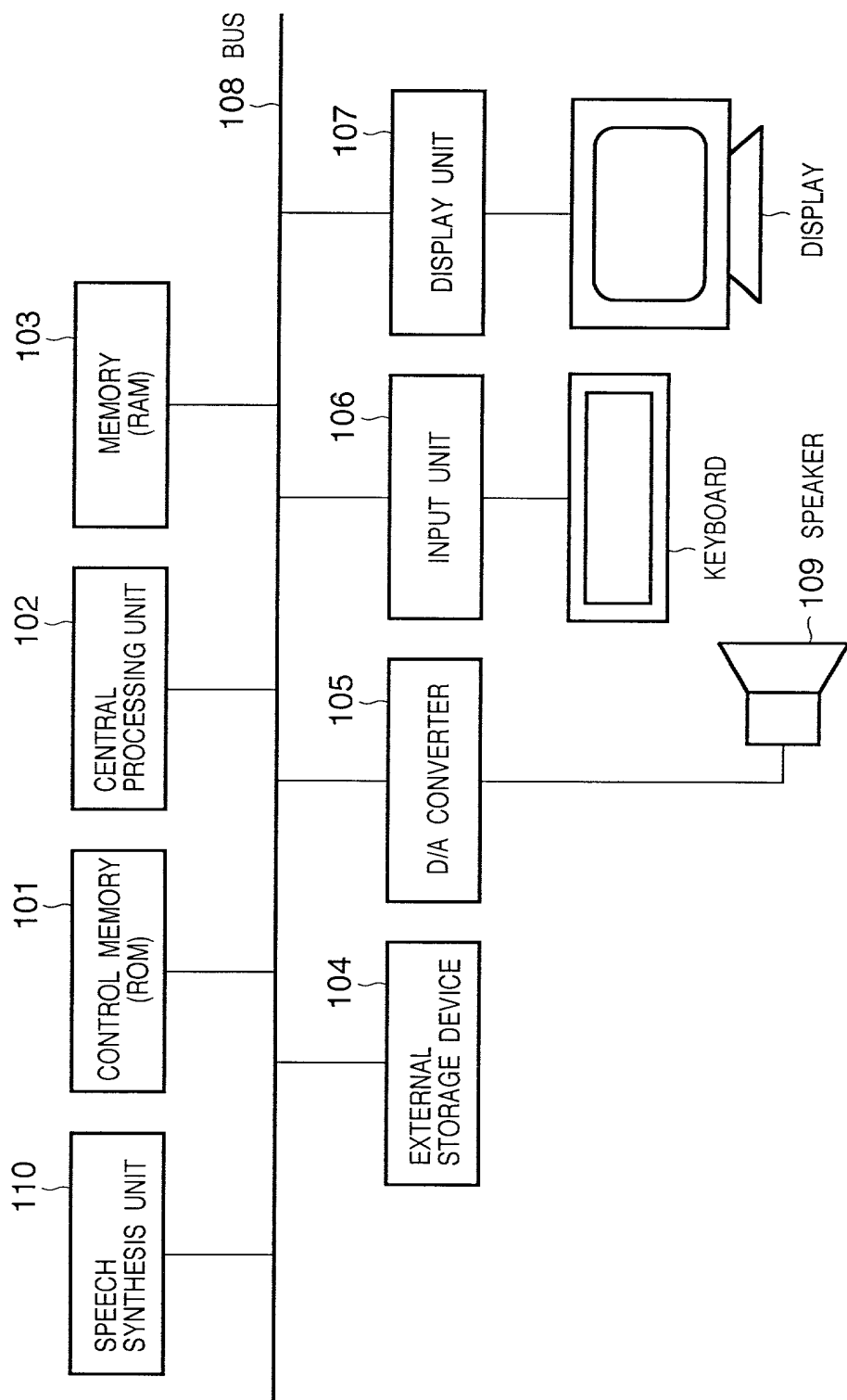
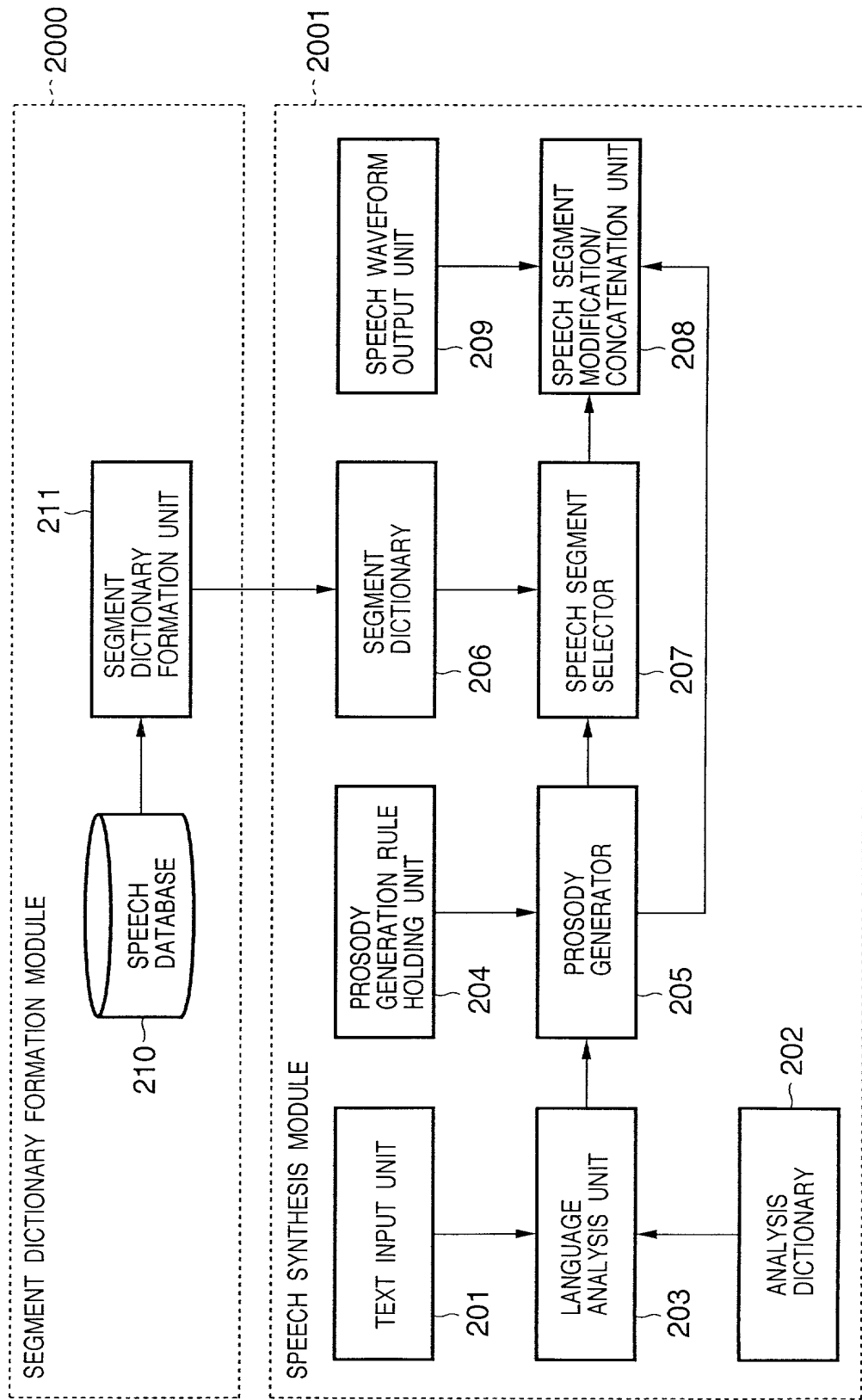


FIG. 2



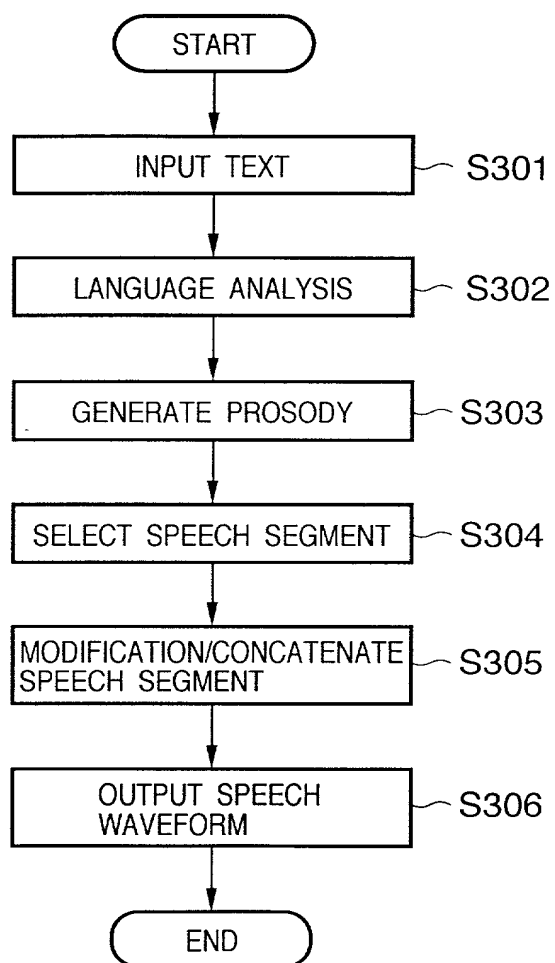
**FIG. 3**

FIG. 4

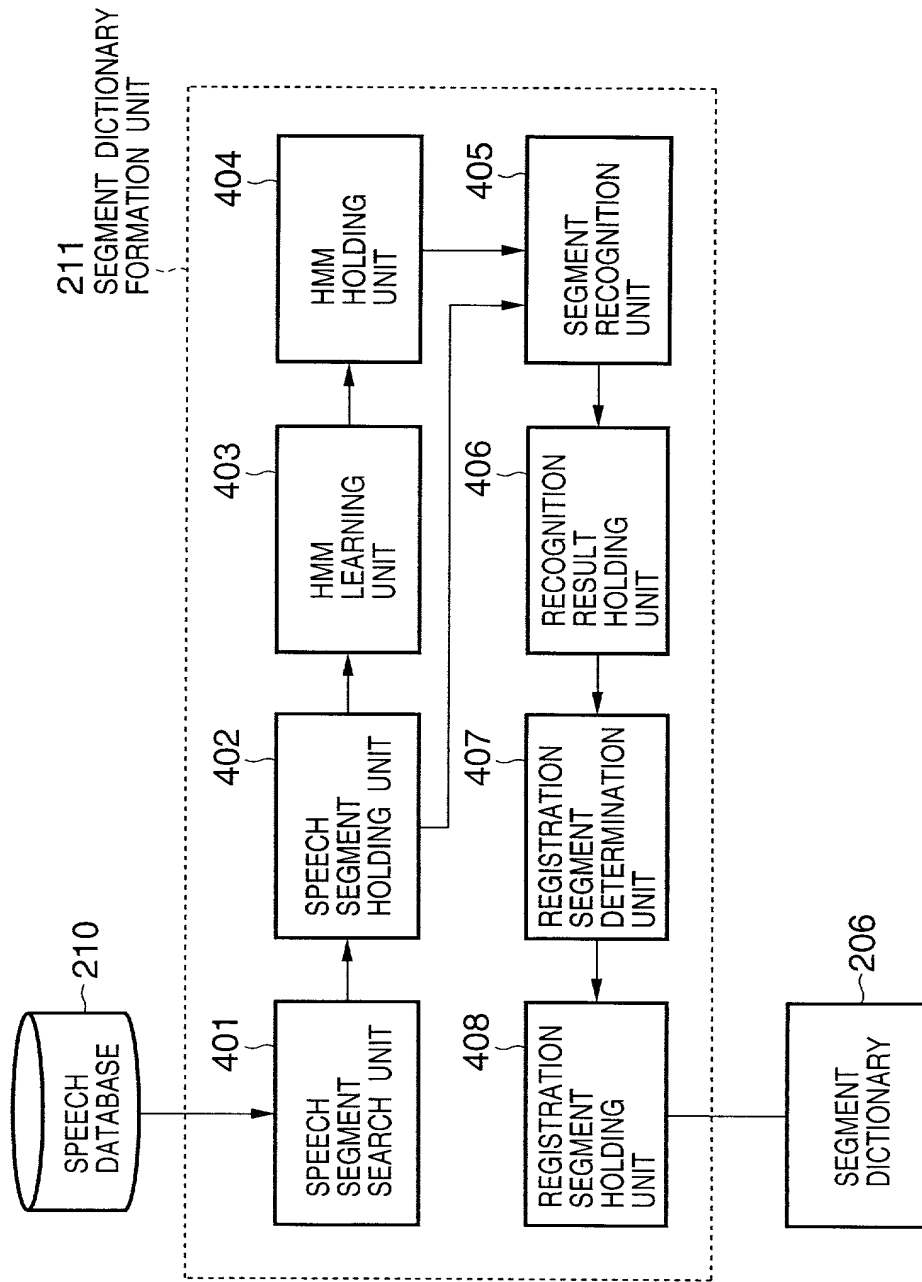
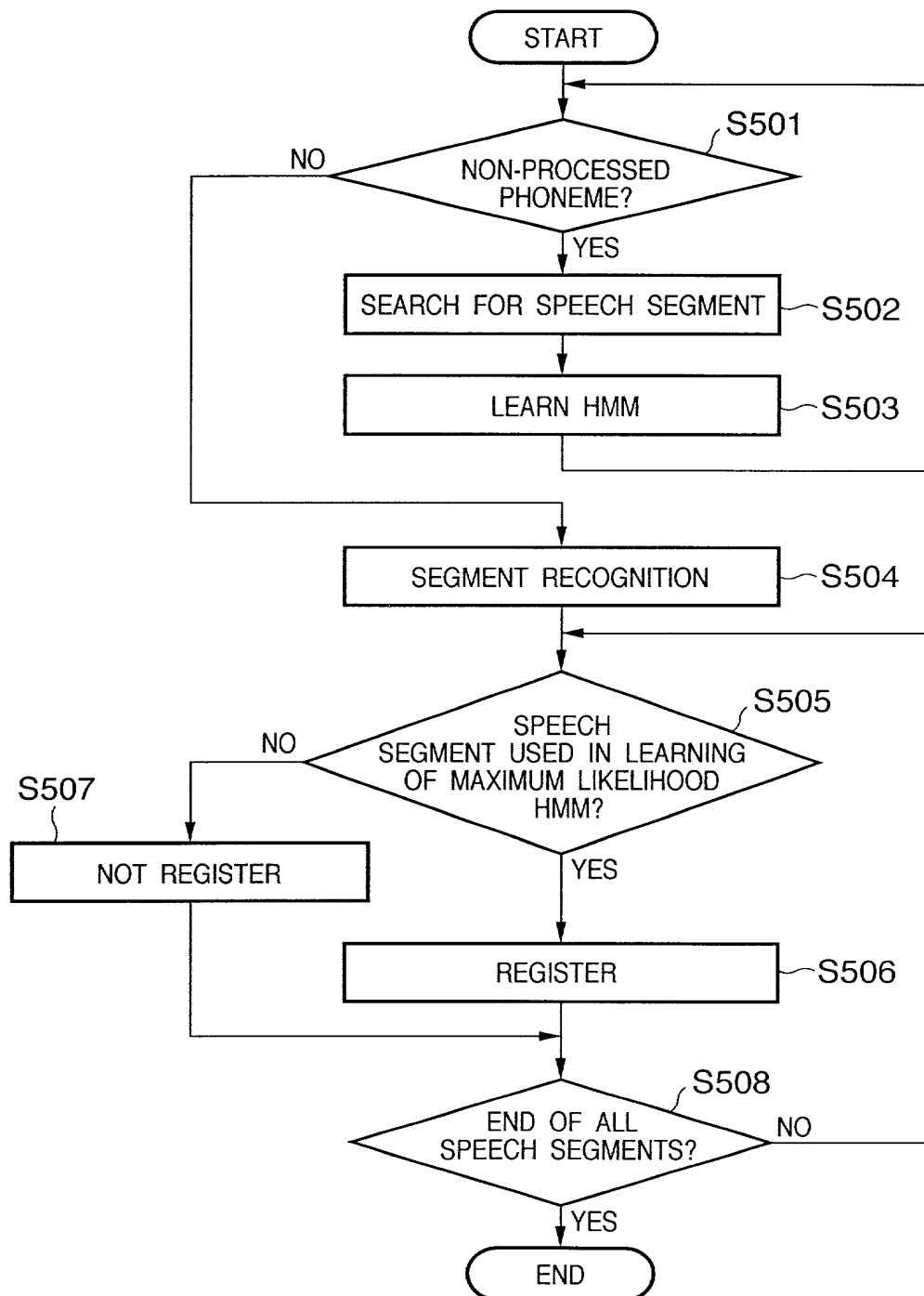


FIG. 5



**FIG. 6**

RECOGNITION TARGET	ALLOWABLE PATTERN
a.y	a.i
a.k	a.pa.t
...	
i.n	...
...	

**FIG. 7**